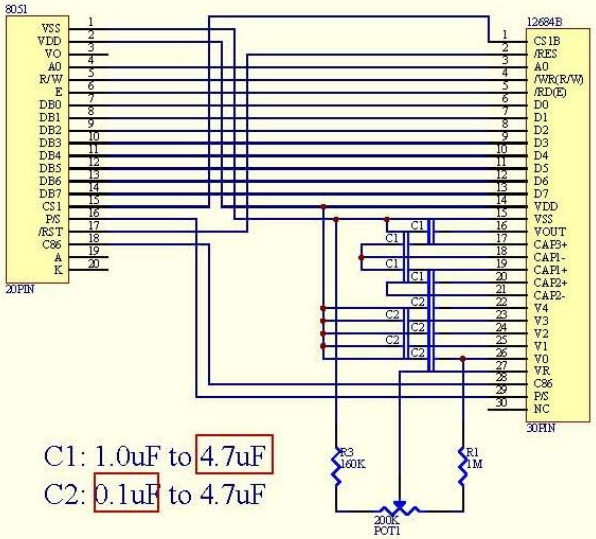
SW101286400B0XXN0100

Adapter Board for Graphic COG LCD WO12864-xxxB#FC / WO12864-xxxB1#FC family

CNN5231XXXXXXXXXXX00	30 PIN;Top Contact;Pitch:0.5mm	JST	30FKZ-SM1-2-TB
CNN6231XXXXXXXXXXX00	30 PIN;Bottom Contact;Pitch:0.5mm	JST	30FKZ-RSM1-2-TB



When using serial interface: 4-LINE

D7=SDA: Serial data input.

D6=SCL: Serial clock input.

D[5:0] are not used and should connect to “H” by VDD.

When CS1B are non-active (CS1B=“H”),

D[7:0] pins are high impedance.

	P/S	C86	Interface
H	1	1	6800
		0	8080
L	0	X	SPI-4

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