Next Vision Display
Brighten The Future

NEXT VISION DISPLAY

SPECIFICATION

CUSTOMER : _____

MODULE NO.: NT024-N001DL

Version	1.1
Engineer	
Date	2016-07-14

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Customer:
Approved by

NEXT VISION DISPLAY CO., LTD.

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1. Scope

This drawing is to introduce the specification of NV2403320F240#A003 active matrix TFT module. It is composed of a color TFT-LCD panel, driver ICs, FPC and backlight unit. The 2.40'' display area contains 240(RGB)x320 pixels.

2. Application

Digital equipments which need color display, mobile phone, mobile navigator/video systems.

3. General Information

Item	Contents	Unit
Size	2.4	inch
Resolution	240(RGB) x 320	/
Interface	RGB / CPU / SPI interface	/
Technology type	a-si TFT	/
Pixel Configuration	R.G.B. Vertical Stripe	
Outline Dimension (W x H x D)	42.92 x 60.26 x 2.48	mm
Active Area	36.72 x 48.96	mm
Display Mode	Transflective, Normally black	/
Backlight Type	LED	/
Driver IC	ILI9325C	/
Weight	12.4±5	g

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5. Interface signals

Pin	Symbol	Function	Remark
1	VCI	Power Supply for Logic and Booster	
2	IOVCC	I/O Interface Supply voltage	
3	IM0	MCU Interface Select Pin	
4	IM1	MCU Interface Select Pin	
5	IM2	MCU Interface Select Pin	
6	IM3	MCU Interface Select Pin	
7	RESET	Hardware reset	
8	VSYNC	Vertical synchronous signal Fix to the GND level when not in use.	
9	HSYNC	Horizontal synchronous signal Fix to the GND level when not in use.	
10	DOTCLK	Dot Clock Signal In RGB I/F Fix to the GND level when not in use	
11	ENABLE	Data enable single in RGB I/F Low: Select (access enabled) High: Not select (access inhibited) Fix to either IOVcc or GND level when not in use.	
12	DB17	Date Bit	
13	DB16	Date Bit	
14	DB15	Date Bit	
15	DB14	Date Bit	
16	DB13	Date Bit	
17	DB12	Date Bit	
18	DB11	Date Bit	
19	DB10	Date Bit	
20	DB9	Date Bit	
21	DB8	Date Bit	
22	DB7	Date Bit	
23	DB6	Date Bit	
24	DB5	Date Bit	
25	DB4	Date Bit	
26	DB3	Date Bit	
27	DB2	Date Bit	
28	DB1	Date Bit	
29	DB0	Date Bit	
30	SDO	Series data output pin, Let SDO as floating when not used.	
31	SDI	Series data input pin	
32	RD	Read enable in 80-system interface mode Fix to either IOVCC or GND level when not in use	

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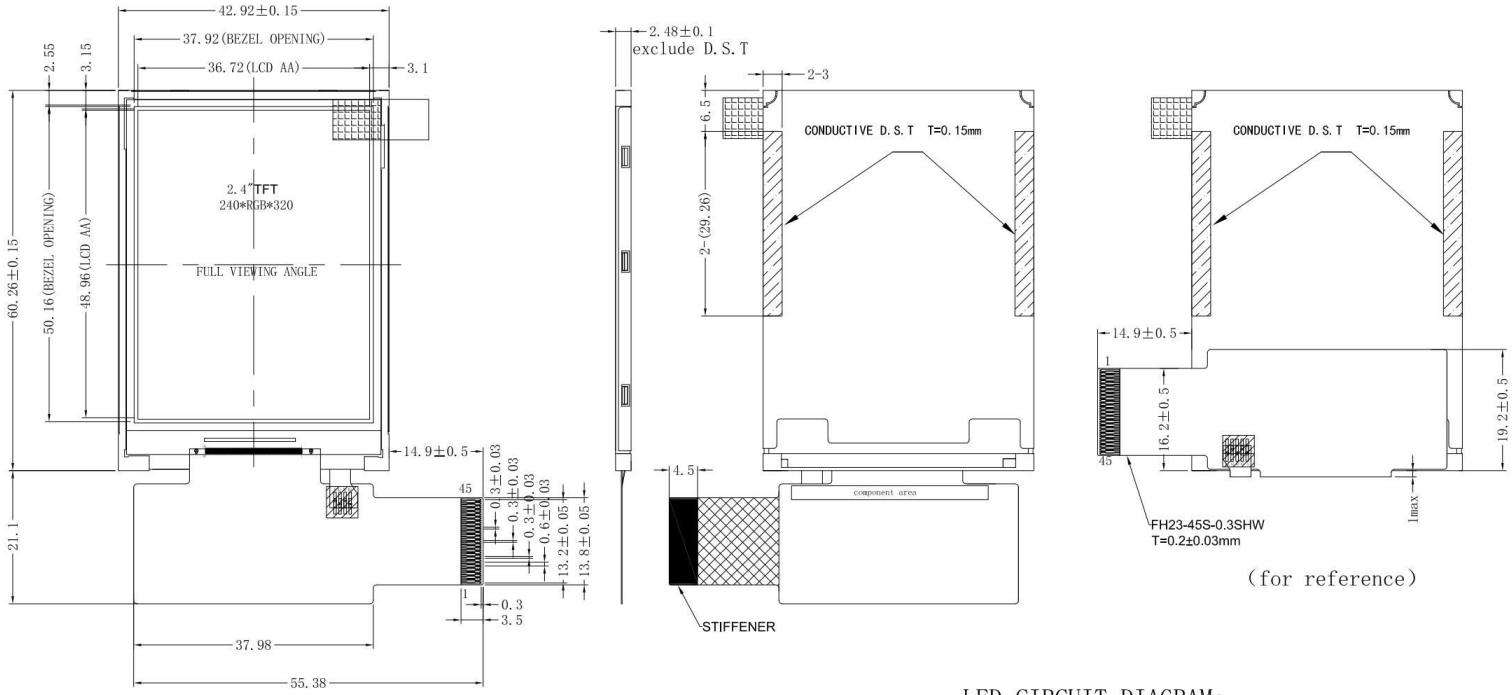
33	WR/SCL	Write Signal Fix to either IOVCC or GND level when not in use. SPI Mode: Synchronizing clock signal in SPI mode.	
34	RS	Register Index or Register Command Select Fix to either IOVCC or GND level when not in use.	
35	CS	Chip Select Pin Fix to the GND level when not in use.	
36	GND	Ground	
37	LEDA	Anode for LED	
38	LEDK1	Cathode for LED	
39	LEDK2	Cathode for LED	
40	NC	No Connection	
41	NC	No Connection	
42	NC	No Connection	
43	NC	No Connection	
44	NC	No Connection	
45	NC	No Connection	

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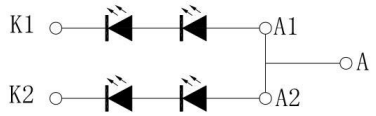
4. Outline Drawing

PIN DEFINATION:

1	VCI
2	IOVCC
3	IM0
4	IM1
5	IM2
6	IM3
7	RESET
8	VSYNC
9	HSYNC
10	DOTCLK
11	ENABLE
12	DB17
13	DB16
14	DB15
15	DB14
16	DB13
17	DB12
18	DB11
19	DB10
20	DB9
21	DB8
22	DB7
23	DB6
24	DB5
25	DB4
26	DB3
27	DB2
28	DB1
29	DB0
30	SDO
31	SDI
32	RD
33	WR/SCL
34	RS
35	CS
36	GND
37	LEDA
38	LEDK1
39	LEDK2
40	NC
41	NC
42	NC
43	NC
44	NC
45	NC



LED CIRCUIT DIAGRAM:



IF=20*2mA VF=6.6V(TYP)

NOTE:

1 DISPLAY TYPE : 2.4" QVGA TFT
2 DISPLAY MODE : TRANSFLECTIVE/NORMALLY BLACK
3 OPERATING TEMP : -20°C--+70°C
4 STORAGE TEMP : -30°C--+80°C
5 DRIVER IC : ILI9325C
6 UNSPECIFIED TOLERANCE ±0.2
7 RoHS compliance

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DRAWN BY:	TITLE: NT024-N001DL		
CHECKED BY:			SCALE:
APPROVED BY:	DWG NO:		UNIT: mm
CONFIRMED BY:	DWG NAME:	SHEET NO:	OF