Winstar Display Co., LTD
華凌光電股份有限公司
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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF32-240320DD#000-FC

APPROVED BY: **(FOR CUSTOMER USE ONLY)**	**PCB VERSION:** **DATA:**

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
first FC components			葉虹蘭
ISSUED DATE: 2020/12/29			

General Specifications

Item	Dimension	Unit
Size	3.2	inch
Dot Matrix	240x RGBx 320(TFT)	dots
Module dimension	55.04 (W) x 77.6 (H) x 2.55(D)	mm
Active area	48.6 x 64.8	mm
Pixel pitch	0.2025 x 0.2025	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	6 o'clock	
Gray Scale Inversion Direction	12 o'clock	
Aspect Ratio	Portrait	
Driver IC	ILI9341 or Equivalent	
Interface	80 MCU 8bit /9bit/16bit/18bit/SPI(3 Wire/4 Wire)	
Backlight Type	LED,Normally White	
With /Without TP	Without TP	
Surface	Anti-Glare	

*Color tone slight changed by temperature and driving voltage.

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Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. ≦60℃, 90% RH MAX. Temp.＞60℃, Absolute humidity shall be less than 90% RH at 60℃

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Interface

LCM PIN Definition

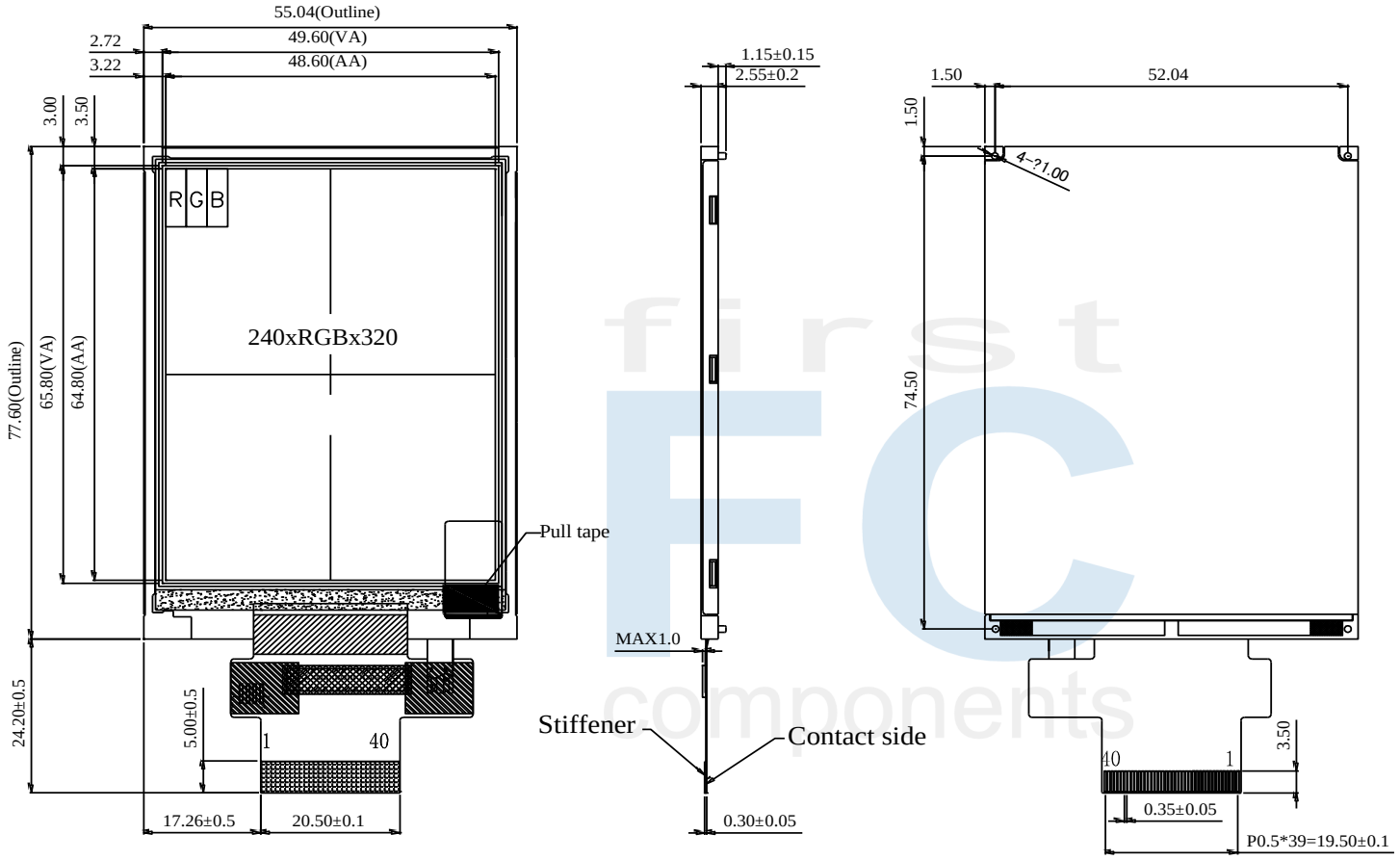
NO	Symbol	Function	I/O
1	NC	No connection	—
2	NC	No connection	—
3	NC	No connection	—
4	NC	No connection	—
5	VCI	Power supply(TYP: 2.8V).	P
6	IOVCC	Power supply(TYP:1.8V/2.8V).	P
7	R5	18-bit parallel bi-directional data bus for MCU system and RGB interface mode Fix to VSS level when not in use	I/O
8	R4		
9	R3		
10	R2		
11	R1		
12	R0		
13	G5		
14	G4		
15	G3		
16	G2		
17	G1		
18	G0		
19	B5		
20	B4		
21	B3		
22	B2		
23	B1		
24	B0		
25	SDA	Serial data input/output	I/O
26	DOTCLK	Data enable signal in RGB interface.	I
27	DE	A data ENABLE signal in RGB I/F mode	I
28	HSYNC	Horizontal synchronizing signal in RGB interface	I
29	VSYNC	Vertical synchronizing signal in RGB interface	I
30	NRD	Read enable pin I80 parallel bus system interface	I
31	NWR_DNC	NWR Write enable pin I80 parallel bus system interface	I

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<table>
<tr><td></td><td></td><td>DNC Command/parameter or display data selection pin in serial bus system interface</td><td></td></tr>
<tr><td>32</td><td>DNC_SCL</td><td>DNC Command/parameter or display data selection pin in parallel interface
SCL Serial data clock in serial bus system Interface</td><td>I</td></tr>
<tr><td>33</td><td>NCS</td><td>Chip select signal</td><td>I</td></tr>
<tr><td>34</td><td>NRESET</td><td>System Reset</td><td>I</td></tr>
<tr><td>35
36
37</td><td>IM0
IM1
IM2</td><td>System interface select:
<table>
<tr><th rowspan="2">IM2</th><th rowspan="2">IM1</th><th rowspan="2">IM0</th><th rowspan="2">MCU-Interface Mode</th><th colspan="2">DB Pin in use</th></tr>
<tr><th>Register/Content</th><th>GRAM</th></tr>
<tr><td>0</td><td>0</td><td>0</td><td>80 MCU 8-bit bus interface I</td><td>D[7:0]</td><td>D[7:0]</td></tr>
<tr><td>0</td><td>0</td><td>1</td><td>80 MCU 16-bit bus interface I</td><td>D[7:0]</td><td>D[15:0]</td></tr>
<tr><td>0</td><td>1</td><td>0</td><td>80 MCU 9-bit bus interface I</td><td>D[7:0]</td><td>D[8:0]</td></tr>
<tr><td>0</td><td>1</td><td>1</td><td>80 MCU 18-bit bus interface I</td><td>D[7:0]</td><td>D[17:0]</td></tr>
<tr><td>1</td><td>0</td><td>1</td><td>3-wire 9-bit data serial interface I</td><td colspan="2">SDA: In/OUT</td></tr>
<tr><td>1</td><td>1</td><td>0</td><td>4-wire 8-bit data serial interface I</td><td colspan="2">SDA: In/OUT</td></tr>
</table></td><td>I</td></tr>
<tr><td>38</td><td>GND</td><td>Ground</td><td>P</td></tr>
<tr><td>39</td><td>VLED+</td><td>Anode of LED backlight.</td><td>P</td></tr>
<tr><td>40</td><td>VLED-</td><td>Cathode of LED backlight.</td><td>P</td></tr>
</table>

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10.Contour Drawing



NO.	SYMBOL
1	NC
2	NC
3	NC
4	NC
5	VCI
6	IOVCC
7	R5
8	R4
9	R3
10	R2
11	R1
12	R0
13	G5
14	G4
15	G3
16	G2
17	G1
18	G0
19	B5
20	B4
21	B3
22	B2
23	B1
24	B0
25	SDA
26	DOTCLK
27	DE
28	HSYNC
29	VSYNC
30	NRD
31	NWR_DNC
32	DNC_SCL
33	NCS
34	NRESET
35	IM0
36	IM1
37	IM2
38	GND
39	VLED+
40	VLED-

The non-specified tolerance of dimension is ±0.3mm.