Winstar Display Co., LTD
華凌光電股份有限公司
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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF43-480272WD#0PW3-FC

APPROVED BY: **(FOR CUSTOMER USE ONLY)**	**PCB VERSION:** **DATA:**

SALES BY	**APPROVED BY**	**CHECKED BY**	**PREPARED BY**
first FC components			葉虹蘭
ISSUED DATE: 2019/09/19			

General Specifications

Item	Dimension	Unit
Size	4.3	inch
Dot Matrix	480 x RGBx272(TFT)	dots
Module dimension	105.5(W) x 67.2(H) x 4.6(D)	mm
Active area	95.04 x 53.856	mm
Dot pitch	0.066 x 0.198	mm
LCD type	TFT, Normally Black, Transmissive	
Viewing Angle	80/80/80/80	
Aspect Ratio	16:9	
TFT Driver IC	SC7283 or equivalent	
Interface	24-bit RGB	
Backlight Type	LED,Normally White	
CTP FW Version	0X01	
CTP Interface	I2C	
CTP IC	FT5426 or equivalent	
With /Without TP	With CTP	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+80	℃
Storage Temperature	TST	-30	—	+80	℃

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. ≦60℃, 90% RH MAX. Temp.＞60℃, Absolute humidity shall be less than 90% RH at 60℃

Interface

1. LCM PIN Definition

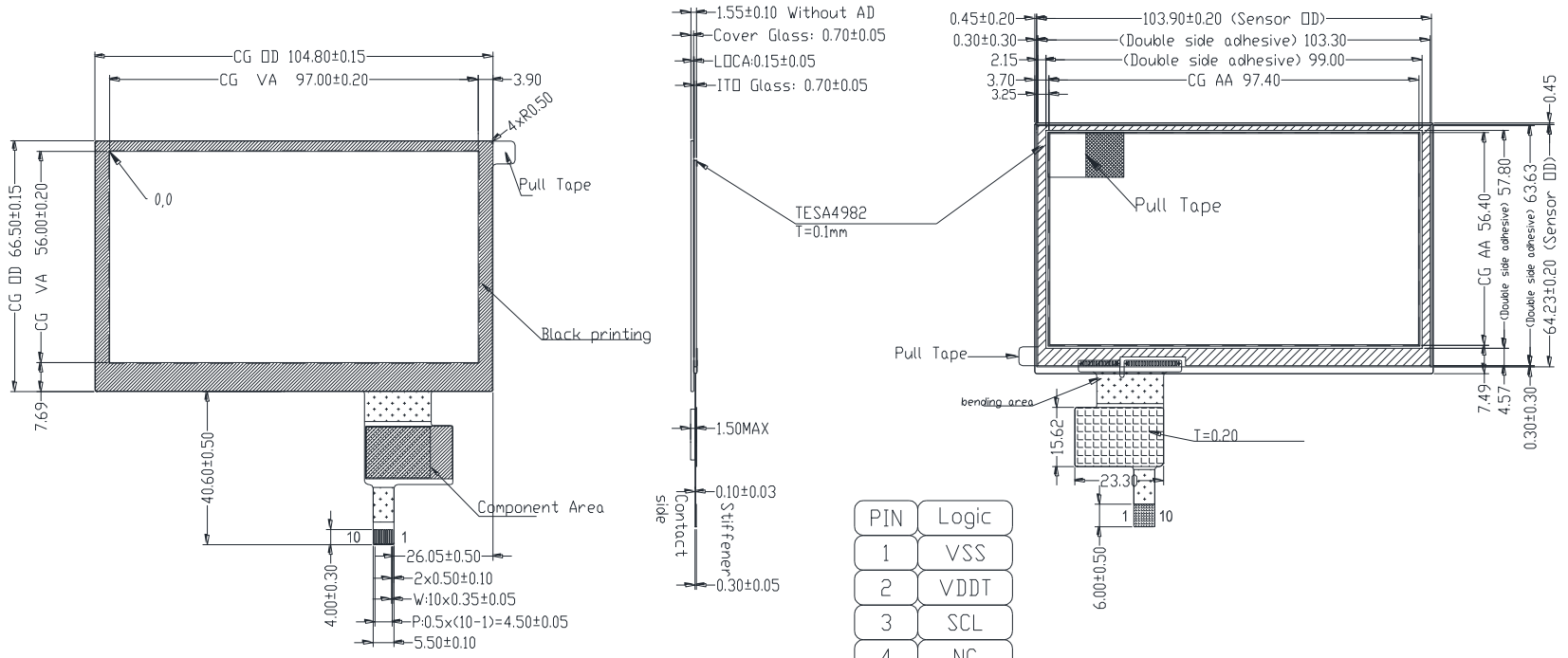
Pin	Symbol	Function	Remark
1	LED-	Power for LED backlight cathode	
2	LED+	Power for LED backlight anode	
3	GND	Power ground	
4	VCC	Power voltage	
5	R0	Red data	
6	R1	Red data	
7	R2	Red data	
8	R3	Red data	
9	R4	Red data	
10	R5	Red data	
11	R6	Red data	
12	R7	Red data	
13	G0	Green data	
14	G1	Green data	
15	G2	Green data	
16	G3	Green data	
17	G4	Green data	
18	G5	Green data	
19	G6	Green data	
20	G7	Green data	
21	B0	Blue data	
22	B1	Blue data	
23	B2	Blue data	
24	B3	Blue data	
25	B4	Blue data	
26	B5	Blue data	
27	B6	Blue data	
28	B7	Blue data	
29	GND	Power ground	
30	CLK	Pixel clock input pin	

<table>
<tr><td>31</td><td>DISP</td><td>DISP sets the display mode.<table><tr><th>VDLKPOL</th><th>Function Description</th></tr><tr><td>L</td><td>VSYNC polarity: positive</td></tr><tr><td>H</td><td>YSNC polarity: negative</td></tr></table></td><td></td></tr>
<tr><td>32</td><td>HSYNC</td><td>Horizontal sync signal, default is negative polarity.</td><td></td></tr>
<tr><td>33</td><td>VSYNC</td><td>Vertical sync signal, default is negative polarity.</td><td></td></tr>
<tr><td>34</td><td>DE</td><td>Data input enable.</td><td></td></tr>
<tr><td>35</td><td>NC</td><td>No connection</td><td></td></tr>
<tr><td>36</td><td>GND</td><td>Power ground</td><td></td></tr>
<tr><td>37</td><td>NC</td><td>No connection</td><td></td></tr>
<tr><td>38</td><td>NC</td><td>No connection</td><td></td></tr>
<tr><td>39</td><td>NC</td><td>No connection</td><td></td></tr>
<tr><td>40</td><td>NC</td><td>No connection</td><td></td></tr>
</table>

2. CTP PIN Definition

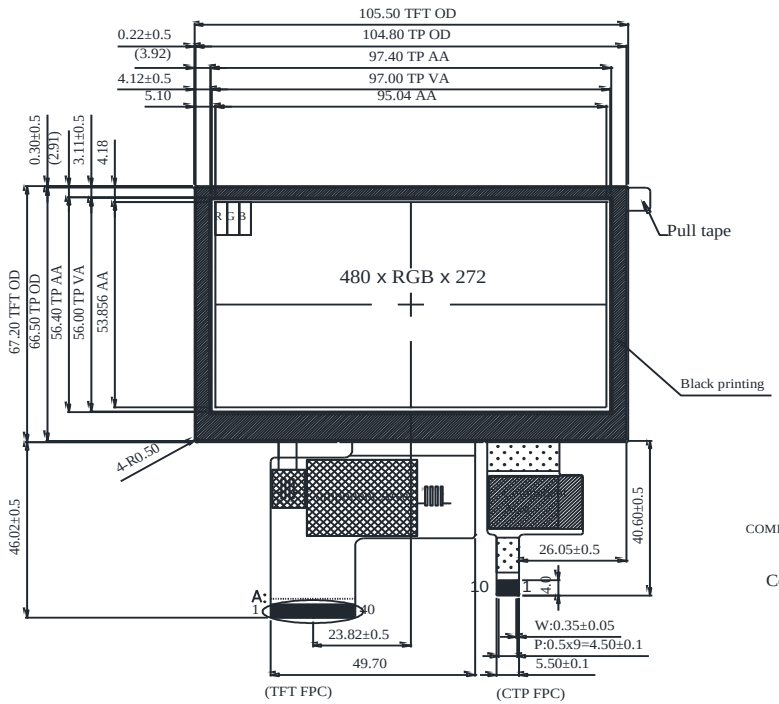
Pin	Symbol	Function	Remark
1	VSS	Ground for analog circuit	
2	VDDT	Power Supply : +3.3V	
3	SCL	I2C clock input	
4	NC	No connection	
5	SDA	I2C data input and output	
6	NC	No connection	
7	/RST	External Reset, Low is active	
8	NC	No connection	
9	/INT	External interrupt to the host	
10	VSS	Ground for analog circuit	

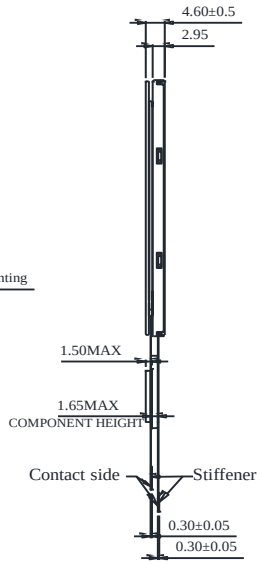
Touch Panel Information

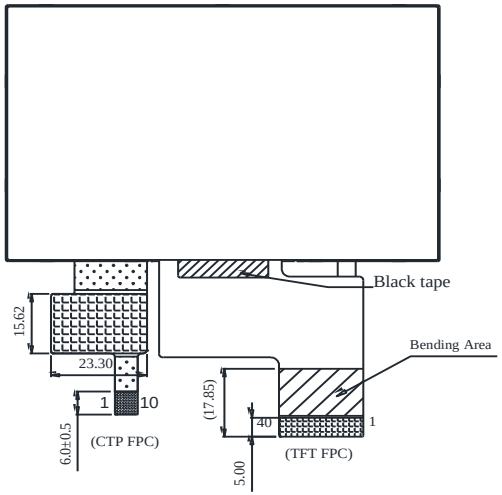


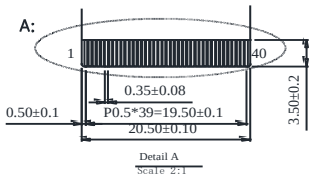
PIN	Logic
1	VSS
2	VDDT
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	VSS

Contour Drawing









CTP	
PIN NO.	SYMBOL
1	VSS
2	VDDT
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	VSS

TFT	
PIN NO.	SYMBOL
1	LED-
2	LED+
3	GND
4	VCC
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	CLK
31	DISP
32	HSYNC
33	VSYNC
34	DE
35	NC
36	GND
37	NC
38	NC
39	NC
40	NC

Note.

380nm transmittance(UV Cut at 380nm)<1.0%

The non-specified tolerance of dimension is ±0.3 mm .