Winstar Display Co., LTD
華凌光電股份有限公司
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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF57-320240YD#004-FC

General Specifications

Item	Dimension	Unit
Size	5.7	inch
Dot Matrix	320 x RGBx240(TFT)	dots
Module dimension	141.12(W) x 101.55(H) x 6.5(D)(MAX)	mm
Active area	115.2 x 86.40	mm
Dot pitch	0.12 x 0.36	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	12 o'clock	
Gray Scale Inversion Direction	6 o'clock	
Backlight Type	LED,Normally White	
With /Without TP	Without TP	
Surface	Glare	

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. ≦60℃, 90% RH MAX. Temp.＞60℃, Absolute humidity shall be less than 90% RH at 60℃

Electrical Characteristics

Operating conditions:

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	VCC	—	3.0	3.3	3.6	V
Supply Current	I_{CC}	VCC=3.3V	—	140	210	mA

LED driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current		-	140	-	mA	
Power Consumption		1260		1470	mW	
LED voltage	VBL+	9.0		10.5	V	Note 1
LED Life Time			50,000		Hr	Note 2,3,4

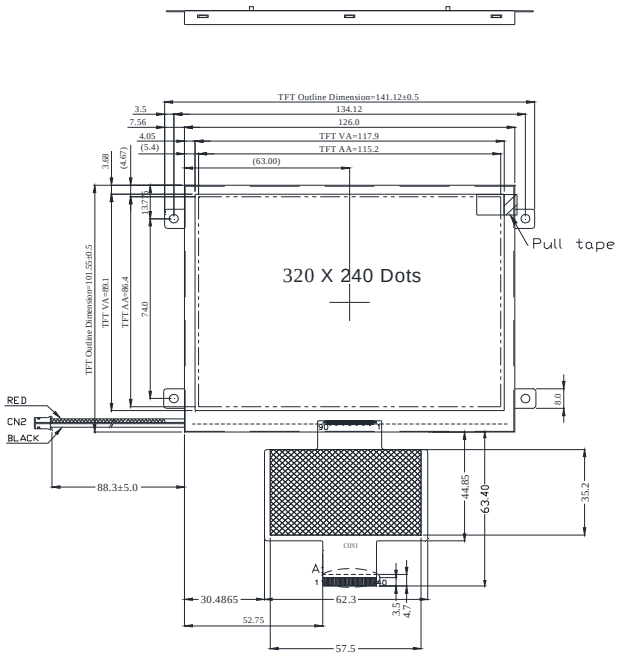
Note 1 : There are 1 Groups LED

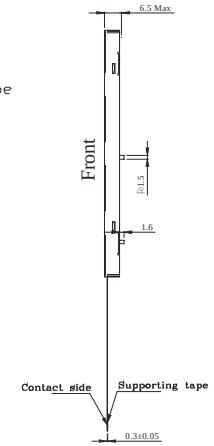
Interface

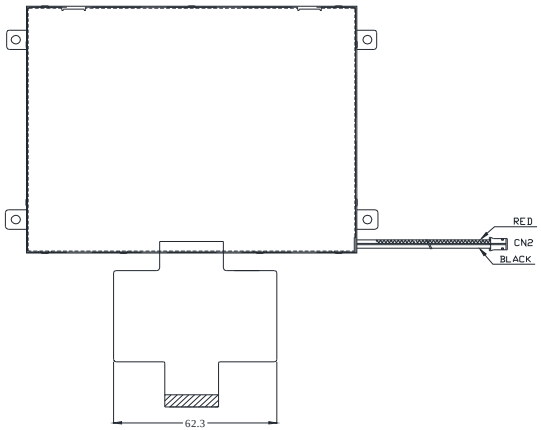
LCM PIN Definition

Pin	Symbol	Function	Remark
1	NC	No connection	
2	NC	No connection	
3	GND	System ground pin of the IC. Connect to system ground.	
4	VCC	Power Supply	
5	R0	Red Data bit(LSB)	
6	R1	Red Data bit	
7	R2	Red Data bit	
8	R3	Red Data bit	
9	R4	Red Data bit	
10	R5	Red Data bit	
11	R6	Red Data bit	
12	R7	Red Data bit (MSB)	
13	G0	Green Data bit(LSB)	
14	G1	Green Data bit	
15	G2	Green Data bit	
16	G3	Green Data bit	
17	G4	Green Data bit	
18	G5	Green Data bit	
19	G6	Green Data bit	
20	G7	Green Data bit (MSB)	
21	B0	Blue Data bit(LSB)	
22	B1	Blue Data bit	
23	B2	Blue Data bit	
24	B3	Blue Data bit	
25	B4	Blue Data bit	
26	B5	Blue Data bit	
27	B6	Blue Data bit	
28	B7	Blue Data bit (MSB)	
29	GND	System ground pin of the IC. Connect to system ground.	
30	CLK	Dot data clock	
31	L/R	Shift direction of device internal shift register control.	Note2,3
32	Hsync	Horizontal sync signal	Note1
33	Vsync	Vertical sync signal	Note1
34	DE	Data Enable signal	Note1
35	U/D	Up/down selection	Note2,3
36	RESET	Hardware reset	
37	NC	No connection	
38	NC	No connection	
39	NC	No connection	
40	NC	No connection	

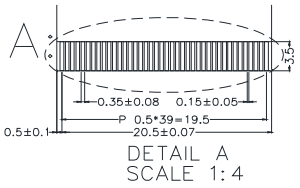
Contour Drawing

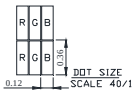






CN2		CON1			
RED	+	PIN NO	SYMBOL	PIN NO	SYMBOL
BLACK	−	1	NC	21	B0
		2	NC	22	B1
		3	GND	23	B2
		4	VCC	24	B3
		5	R0	25	B4
		6	R1	26	B5
		7	R2	27	B6
		8	R3	28	B7
		9	R4	29	GND
		10	R5	30	CLK
		11	R6	31	R/L
		12	R7	32	Hsync
		13	G0	33	Vsync
		14	G1	34	DE
		15	G2	35	U/D
		16	G3	36	RESET
		17	G4	37	NC
		18	G5	38	NC
		19	G6	39	NC
		20	G7	40	NC





The non-specified tolerance of dimension is ±0.3mm.