Winstar Display Co., LTD
華凌光電股份有限公司
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SPECIFICATION

MODULE NO.: WF57-640480FD#000-FC

General Specifications

Item	Dimension	Unit
Size	5.7	inch
Dot Matrix	640 x RGBx480(TFT)	dots
Module dimension	125.0 x 98.8 x 7.5	mm
Active area	115.2 x 86.4	mm
Dot pitch	0.06 x 0.18	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	6 o'clock	
Gray Scale Inversion Direction	12 o'clock	
Backlight Type	LED ,Normally White	
With /Without TP	Without TP	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage.

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Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Power Supply for Logic	VCC-DGND	-0.3	—	7	V
Power Supply for Analog	AVDD-AGND	-0.3	—	13.5	V
Input Voltage	VI	-0.3	—	VDD+0.3	V
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Electrical Characteristics

Operating conditions:

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For LCM	VCC	—	3.0	3.3	3.6	V
Supply Current For LCM	ICC	-	—	15	20	mA
Power Consumption	-	-	—	49.5	72	mW
Power Supply for Analog	AVDD	AVDD-AGND	—	10.0	—	V
Input Voltage	VIH	H Level	0.7VCC	—	VCC	V
	VIL	L Level	0	—	0.3VCC	V
Recommended LC Driving Voltage for 25℃	VGH	(Note)	—	15.0	—	V
	VGL		—	-10.0	—	V
	VCOM		—	3.7	—	V

Note 1: This value is test for VDD=3.3V , Ta=25 ℃ only

(1) VGH is TFT Gate on operating Voltage.

(2) VGH is TFT Gate off operating Voltage ,VGL signal must be fluctuates with same phase as VCOM when Storage on Gate structure.

(3) VCOM must be adjusted to optimize display quality Crosstalk , Contrast Ratio and etc

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Interface

LCM PIN Definition

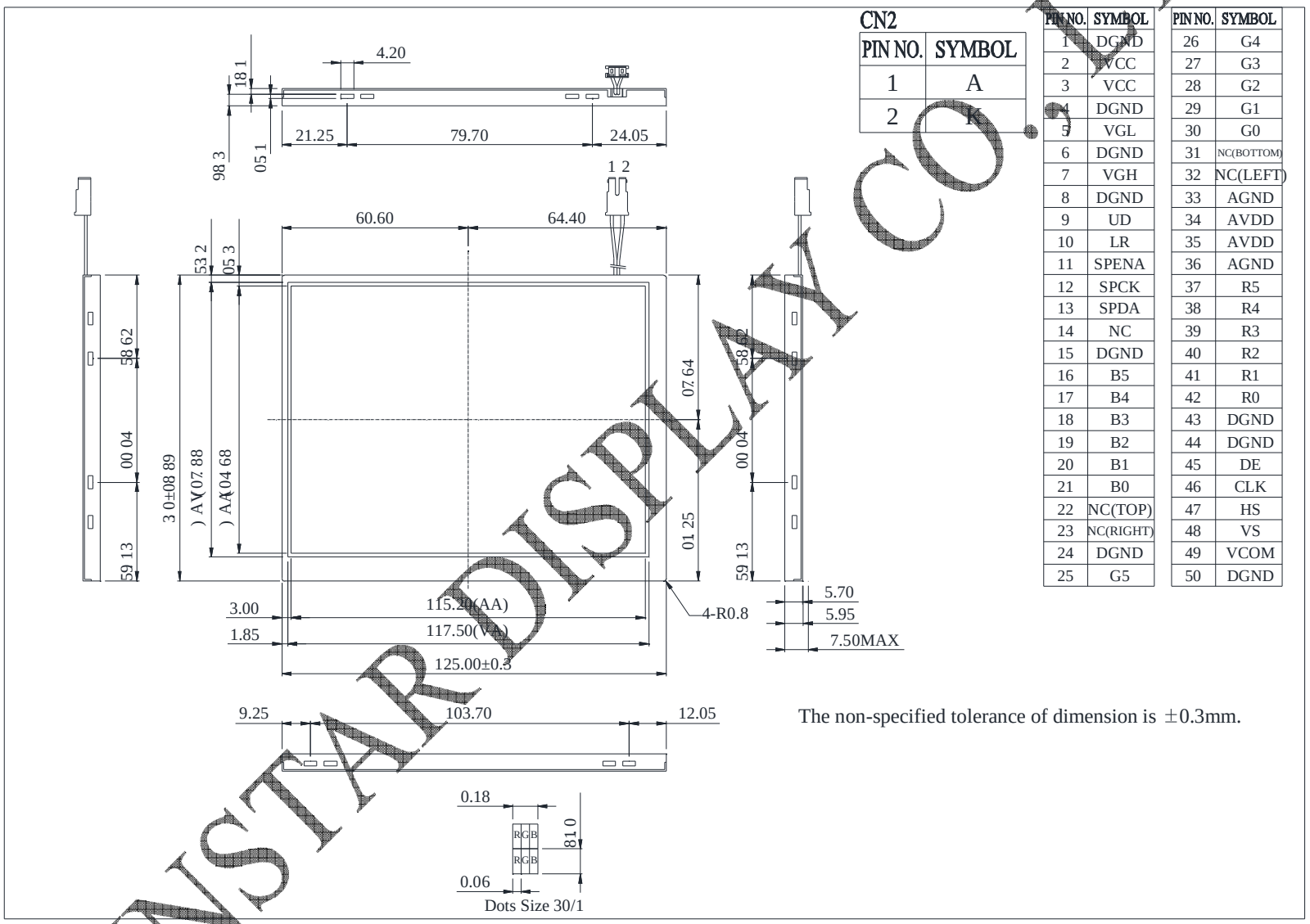
Pin	Symbol	Function
1	DGND	Ground for digital circuit
2	VCC	Power Supply : +3.3V
3	VCC	Power Supply : +3.3V
4	DGND	Ground for digital circuit
5	VGL	Gate off power
6	DGND	Ground for digital circuit
7	VGH	Gate on power
8	DGND	Ground for digital circuit
9	UD	Up/down selection
10	LR	Left /right selection
11	SPENA	Chip select
12	SPCK	Serial Clock
13	SPDA	Serial Data
14	NC	No connect
15	DGND	Ground for digital circuit
16	B5	Blue Data bus
17	B4	Blue Data bus
18	B3	Blue Data bus
19	B2	Blue Data bus
20	B1	Blue Data bus
21	B0	Blue Data bus
22	NC(TOP)	No connection
23	NC(RIGHT)	No connection
24	DGND	Ground for digital circuit
25	G5	Green Data bit
26	G4	Green Data bit
27	G3	Green Data bit
28	G2	Green Data bit
29	G1	Green Data bit
30	G0	Green Data bit
31	NC(BOTTOM)	No connection
32	NC(LEFT)	No connection
33	AGND	Ground
34	AVDD	Analog power: 10V
35	AVDD	Analog power: 10V

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36	AGND	Ground
37	R5	Red Data bit
38	R4	Red Data bit
39	R3	Red Data bit
40	R2	Red Data bit
41	R1	Red Data bit
42	R0	Red Data bit
43	DGND	Ground for digital circuit
44	DGND	Ground for digital circuit
45	DE	Data Enable input
46	CLK	Data Clock
47	HS	Horizontal sync input
48	VS	Vertical sync input
49	VCOM	VCOM driving input
50	DGND	Ground for digital circuit

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Contour Drawing



CN2

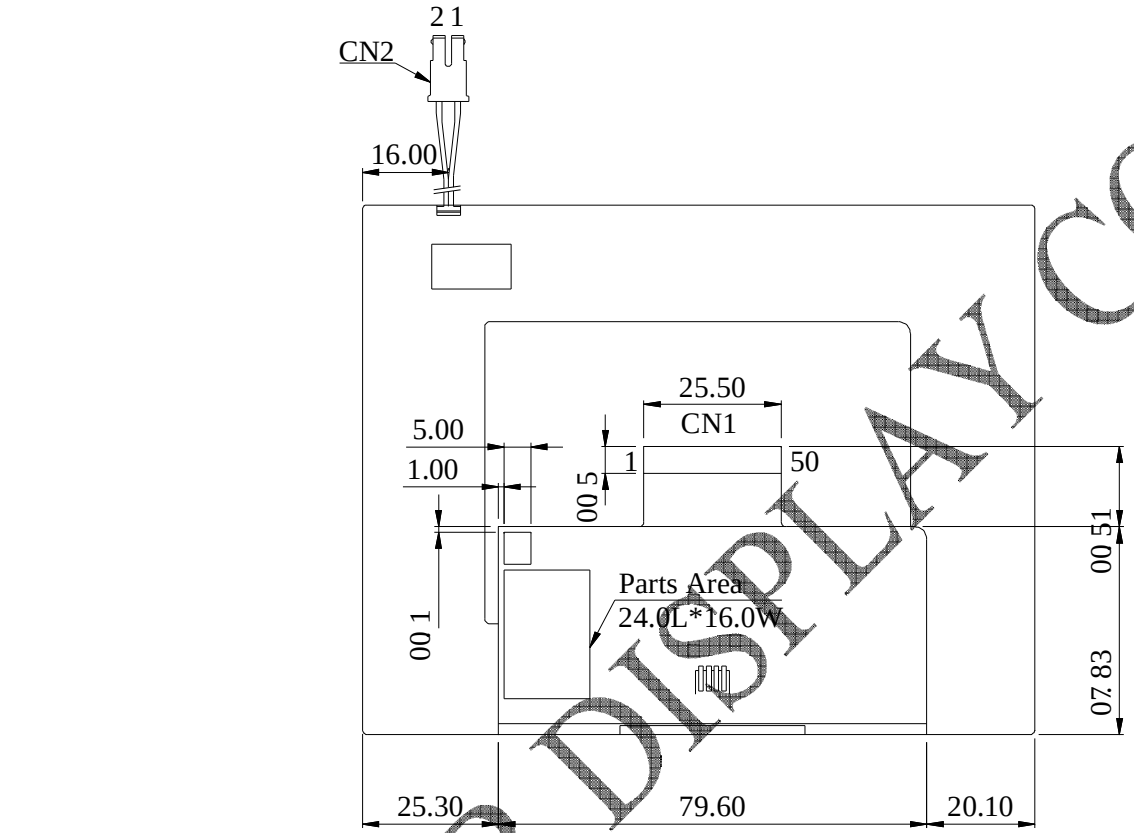
PIN NO.	SYMBOL
1	A
2	K

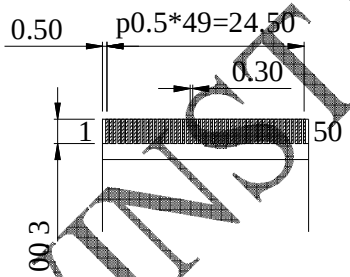
PIN NO.	SYMBOL
1	DGND
2	VCC
3	VCC
4	DGND
5	VGL
6	DGND
7	VGH
8	DGND
9	UD
10	LR
11	SPENA
12	SPCK
13	SPDA
14	NC
15	DGND
16	B5
17	B4
18	B3
19	B2
20	B1
21	B0
22	NC(TOP)
23	NC(RIGHT)
24	DGND
25	G5

PIN NO.	SYMBOL
26	G4
27	G3
28	G2
29	G1
30	G0
31	NC(BOTTOM)
32	NC(LEFT)
33	AGND
34	AVDD
35	AVDD
36	AGND
37	R5
38	R4
39	R3
40	R2
41	R1
42	R0
43	DGND
44	DGND
45	DE
46	CLK
47	HS
48	VS
49	VCOM
50	DGND

The non-specified tolerance of dimension is ±0.3mm.

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