Winstar Display Co., LTD
華凌光電股份有限公司
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SPECIFICATION

MODULE NO.: WF101-1024600AL#Z00-FC

General Specifications

Item	Dimension	Unit
Size	10.1	inch
Dot Matrix	1024 RGB X 600	dots
Module dimension	235(W) x143(H) x 3.0(D)	mm
Active area	222.72 (H) x 125.28(V)	mm
Dot pitch	0.2175(W) x 0.2088(H)	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	12 o'clock	
Gray Scale Inversion Direction	6 o'clock	
Backlight Type	LED,Normally White	
With /Without TP	Without TP	
Surface	Anti-Glare	

*Color tone slight changed by temperature and driving voltage.

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Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-10	—	+60	℃
Storage Temperature	TST	-20	—	+70	℃

Electrical Characteristics

Typical Operation Conditions (At Ta = 25 °C)

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Digital Power Supply Voltage For LCD	VDD	3	3.3	3.6	V
Analog Power Supply Voltage	AVDD	--	10.4	11	V
Gate On Power Supply Voltage	VGH	20	21	22	V
Gate Off Power Supply Voltage	VGL	-8.5	-8	-7	V
Common Power Supply Voltage	VCOM	--	3.8	--	V
Logic Input Voltage	VIH	0.7*DVDD	-	DVDD	V
	VIL	GND	-	0.3*DVDD	V

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Interface

TFT LCD MODULE

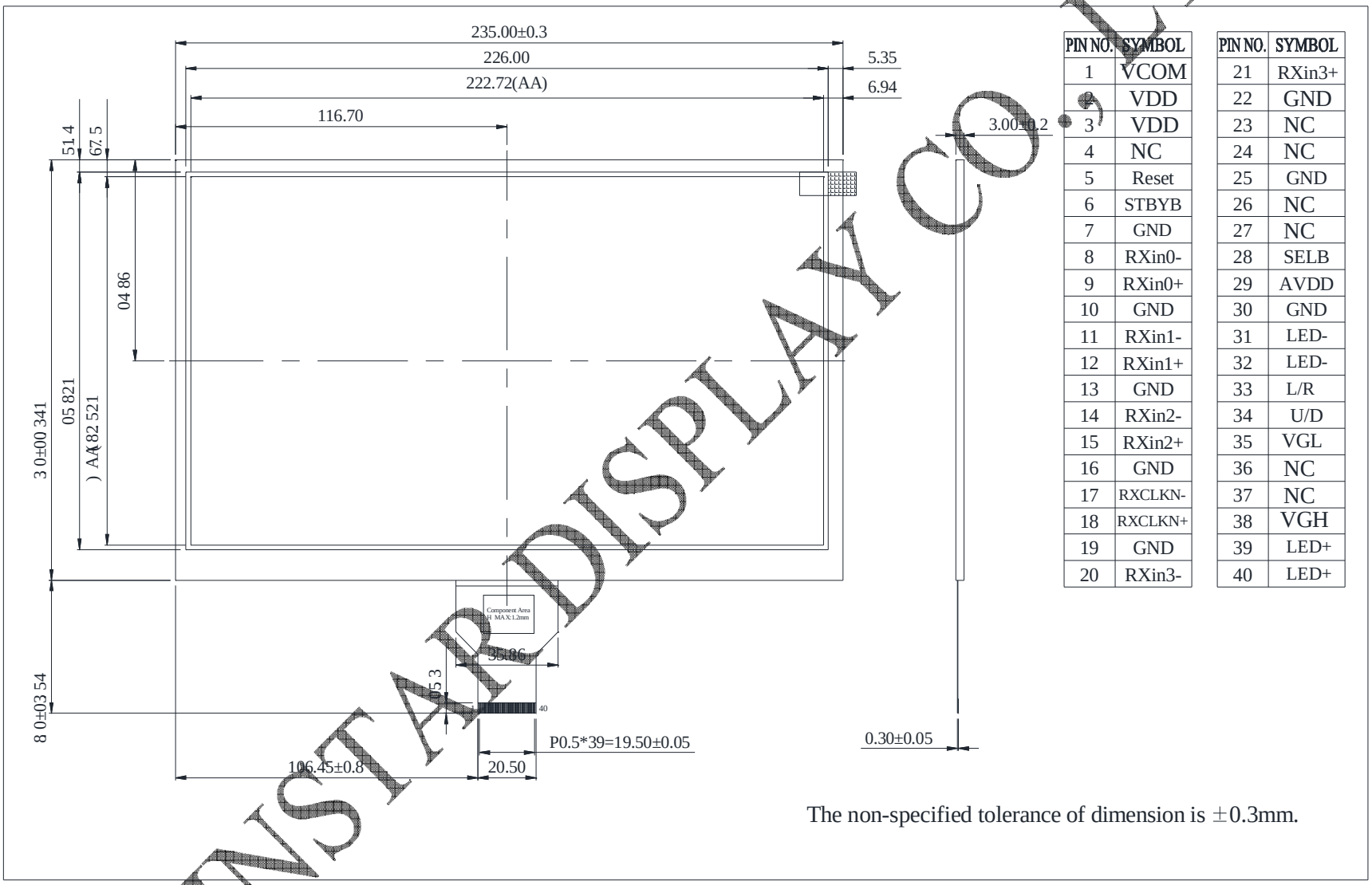
PinNo.	Symbol	Description
1	VCOM	Common voltage
2	VDD	Digital power
3	VDD	Digital power
4	NC	Not connect
5	RESET	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high. (R=10KΩ, C=1μF)
6	STBYB	Standby mode, normally pull high STBYB=”1”, normal operation STBYB=”0”,timing control, source driver will turn off, all output are high-Z
7	GND	Digital ground
8	NIND0	Negative LVDS differential data inputs
9	PIND0	Positive LVDS differential data inputs
10	GND	Digital ground
11	NIND1	Negative LVDS differential data inputs
12	PIND1	Positive LVDS differential data inputs
13	GND	Digital ground
14	NIND2	Negative LVDS differential data inputs
15	PIND2	Positive LVDS differential data inputs
16	GND	Digital ground
17	NINC	Negative LVDS differential clock inputs
18	PINC	Positive LVDS differential clock inputs
19	GND	Digital ground
20	NIND3	Negative LVDS differential data inputs
21	PIND3	Positive LVDS differential data inputs
22	GND	Digital ground
23	NC	Not connect
24	NC	Not connect
25	GND	Digital ground
26	NC	Not connect
27	NC	Not connect
28	SELB	6-bit/8-bit input select SELB = L , 8-bit ; SELB = H , 6-bit
29	AVDD	Analog power
30	GND	Digital ground
31	VLED-	LED Cathode
32	VLED-	LED Cathode
33	SHLR	Left or right display control

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34	UPDN	Up / down display control
35	VGL	Negative power for TFT
36	NC	Not connect
37	NC	Not connect
38	VGH	Positive power for TFT
39	VLED+	LED Anode
40	VLED+	LED Anode

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Contour Drawing

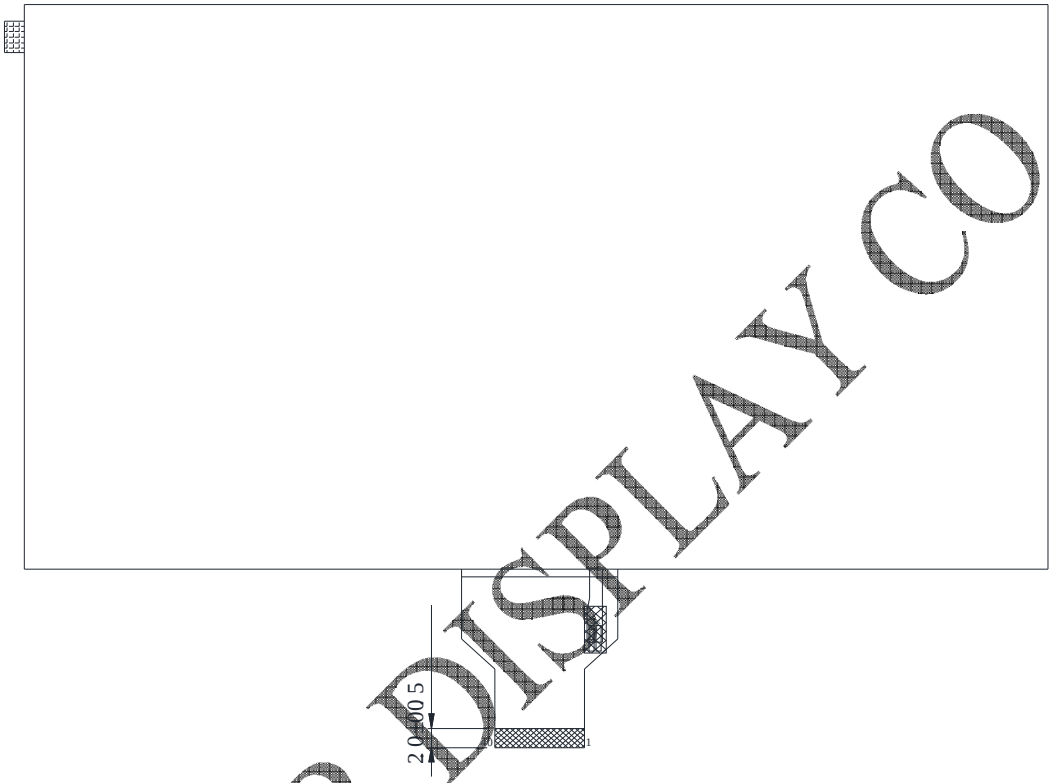


PIN NO.	SYMBOL
1	VCOM
2	VDD
3	VDD
4	NC
5	Reset
6	STBYB
7	GND
8	RXin0-
9	RXin0+
10	GND
11	RXin1-
12	RXin1+
13	GND
14	RXin2-
15	RXin2+
16	GND
17	RXCLKN-
18	RXCLKN+
19	GND
20	RXin3-

PIN NO.	SYMBOL
21	RXin3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	NC
28	SELB
29	AVDD
30	GND
31	LED-
32	LED-
33	L/R
34	U/D
35	VGL
36	NC
37	NC
38	VGH
39	LED+
40	LED+

The non-specified tolerance of dimension is ±0.3mm.

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The non-specified tolerance of dimension is ±0.3mm.

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