SMART DISPLAY SPECIFICATION



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SPECIFICATION

CUSTOMER : _____

MODEL NO. : WL0F00101000JGAACSA02-FC

APPROVED BY: (FOR CUSTOMER USE ONLY)	

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
first FC components	Ginger Xu	Eason Chang	Jason Chan

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2024/12/12		First issue

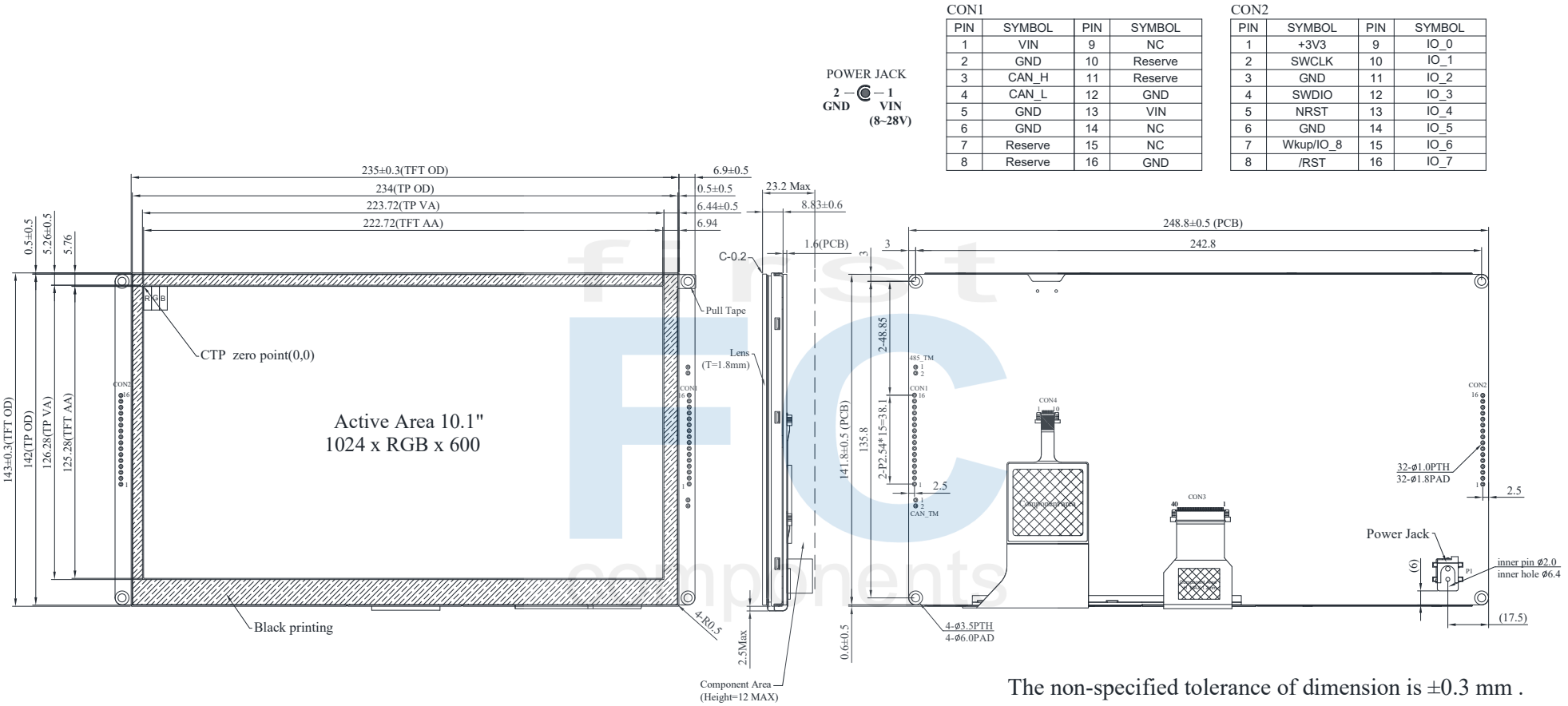
Product information

General information

Item	Standard Value	Unit
Operating voltage	8V~28V dynamic	Vdc
Communication Interface	CAN bus	Vpp
MCU	STM32F746	N/A
Flash Memory	16	MB
LCD display size	10.1	inch
Dot Matrix	1024 x RGBx600(TFT)	dot
Module dimension	248.8(W) x 143(H) x 23.2(D)	mm
Active area	222.72 (H) x 125.28(V)	mm
Pixel pitch	0.2175(W) x 0.2088(H)	mm
Brightness	Min:800; Typ:900	
LCD type	TFT, Normally Black, Transmissive	
View Direction	85/85/85/85	
Aspect Ratio	16:9	
With /Without TP	With CTP	
Surface	Glare	

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Contour Drawing



Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above
1. Temp. ≦60℃, 90% RH MAX. Temp.＞60℃, Absolute humidity shall be less than 90% RH at 60℃

Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage	VCC	—	8	12	28	V
Supply LCM current	I	-	-	720	-	mA

BOM

Item	Description	Remark
LCM	WF101-1024600JL#0SP8-FC	
PCBA	SV10010R100JB00N0106	

Interface

CON1 definition:

Pin	Symbol	Function	Remark
1	+12V	Power supply12V input	Input
2	GND	Power supply GND input	Input
3	CAN_H	CAN bus D+	I/O
4	CAN_L	CAN bus D-	I/O
5	GND	Power supply GND input	GND
6	GND	Power supply GND input	GND
7	Reserve	USB_D-	Reserve
8	Reserve	USB_D+	Reserve
9	NC	-	-
10	Reserve	USART RX interface(Reserve)	Reserve
11	Reserve	USART TX interface(Reserve)	Reserve
12	GND	Power supply GND input	GND
13	+12V	Power supply12V input	Input
14	NC	-	-
15	NC	-	-
16	GND	Power supply GND input	Input

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CON2 definition:

Pin	Symbol	Function	Remark
1	+3V3	3.3V power for JTAG interface	Output
2	SWCLK	CLK pin for JTAG interface	Input
3	GND	GND for JTAG interface	Output
4	SWDIO	Data pin for JTAG interface	I/O
5	NRST	Reset pin for JTAG interface	Input
6	GND	GND	Output
7	Wkup/ IO_8	(PA0) for system Resume from suspend (Reserve)	WKup,ADC,Timer,Event,I/O
8	/RST	Reset (active Low)	I
9	IO_0	IO_0	ADC,DAC,Timer,Event,I/O
10	IO_1	IO_1	ADC,Timer,Event,I/O
11	IO_2	IO_2	ADC,Timer,Event,I/O
12	IO_3	IO_3	RST,Timer,Event,I/O
13	IO_4	IO_4	RST,Timer,Event,I/O
14	IO_5	IO_5	ADC,Timer,Event,I/O
15	IO_6	IO_6	RST,Timer,Event,I/O
16	IO_7	IO_7	RST,ADC,Event,I/O

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