SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF101-1024600JL#0SP1-FC

General Specifications

Item	Dimension	Unit
Size	10.1	inch
Dot Matrix	1024 RGB X 600	dots
Module dimension	235(W) x143(H) x 6.65 (D)	mm
Active area	222.72 (H) x 125.28(V)	mm
Dot pitch	0.2175(W) x 0.2088(H)	mm
LCD type	TFT, Normally Black, Transmissive	
Interface	LVDS	
Driver IC	EK79001HN + EK73215BCGA or equivalent	
Viewing Angle	85/85/85/85	
Aspect Ratio	16:9	
Backlight Type	LED,Normally White	
With /Without TP	With RTP	
Surface	Anti-Glare	

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. ≦60℃, 90% RH MAX. Temp.＞60℃, Absolute humidity shall be less than 90% RH at 60℃

Electrical Characteristics

1. Typical Operation Conditions (At Ta = 25 °C,)

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Digital Power Supply Voltage For LCD	VDD	3	3.3	3.6	V	Note1
Analog Power Supply Voltage	AVDD	9.89	10.2	10.5	V	-
Gate On Power Supply Voltage	VGH	19.4	20.0	20.6	V	-
Gate Off Power Supply Voltage	VGL	-10.3	-10.0	-9.7	V	-
Common Power Supply Voltage	VCOM	4.0	4.3	4.6	V	Note2
Input logic high voltage	VIH	0.7 V_{DD}	-	V_{DD}	V	Note3
Input logic low voltage	VIL	0	-	0.3 V_{DD}	V	

Note1:VDD setting should match the signals output voltage (refer to Note 3) of customer's system board.
Note 2.Please adjust VCOM to make the flicker level be minimum.
Note 3:RESET,STBYB,U/D,L/R,SELB

2. Current Consumption

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	IGH	-	0.48	1.0	mA	VGH =20.0V
	IGL	-	1.57	2.5	mA	VGL = -10.0V
	IVDD	-	24	36	mA	VDD =3.3V
	IAVDD	-	13.7	25	mA	AVDD =10.2V

Interface

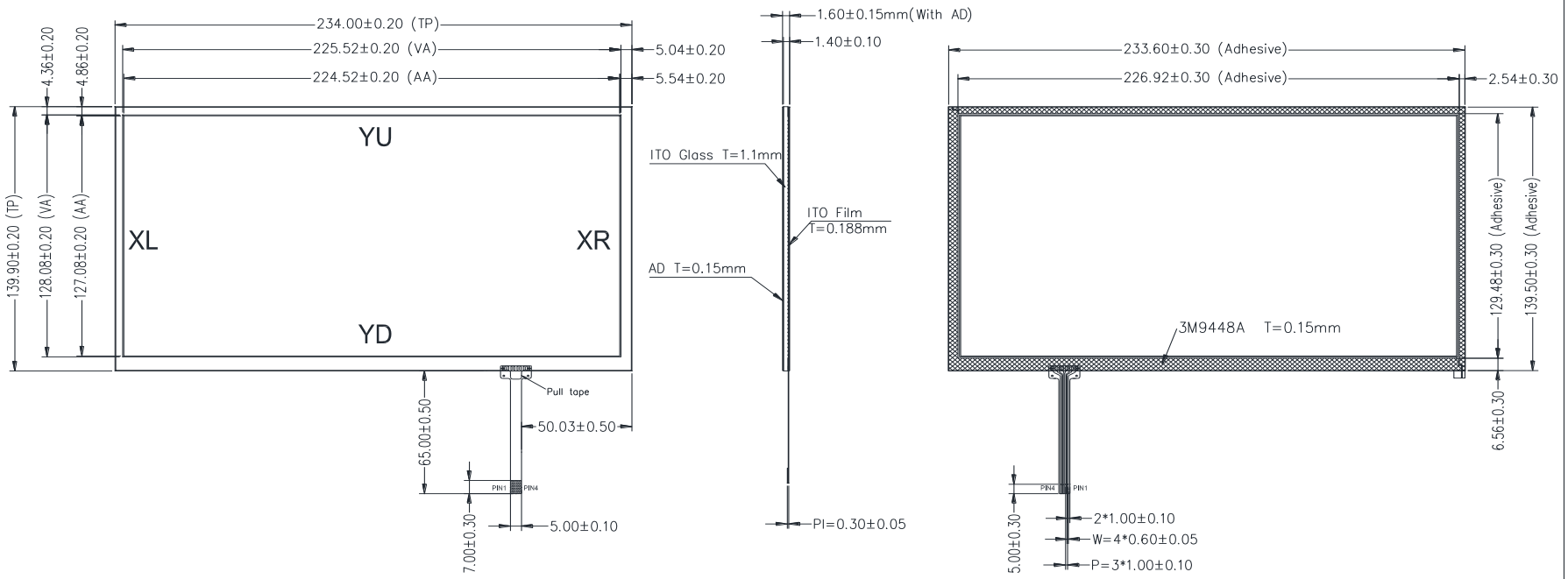
1. TFT LCD MODULE

Pin No.	Symbol	Description
1	VCOM	Common voltage
2	VDD	Digital power
3	VDD	Digital power
4	NC	Not connect
5	Reset	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high. (R=10KΩ，C=1μF)
6	STBYB	Standby mode, normally pull high STBYB=”1”, normal operation STBYB=”0”,timing control, source driver will turn off, all output are high-Z
7	GND	Digital ground
8	RXIN0-	Negative LVDS differential data inputs
9	RXIN0+	Positive LVDS differential data inputs
10	GND	Digital ground
11	RXIN1-	Negative LVDS differential data inputs
12	RXIN1+	Positive LVDS differential data inputs
13	GND	Digital ground
14	RXIN2-	Negative LVDS differential data inputs
15	RXIN2+	Positive LVDS differential data inputs
16	GND	Digital ground
17	RXCLKN-	Negative LVDS differential clock inputs
18	RXCLKN+	Positive LVDS differential clock inputs
19	GND	Digital ground
20	RXIN3-	Negative LVDS differential data inputs
21	RXIN3+	Positive LVDS differential data inputs
22	GND	Digital ground
23	NC	Not connect
24	NC	Not connect
25	GND	Digital ground
26	NC	Not connect
27	NC	Not connect
28	SELB	6-bit/8-bit input select SELB = L , 8-bit ; SELB = H , 6-bit

29	AVDD	Analog power
30	GND	Digital ground
31	LED-	LED Cathode
32	LED-	LED Cathode
33	L/R	Left or right display control
34	U/D	Up / down display control
35	VGL	Negative power for TFT
36	NC	Not connect
37	NC	Not connect
38	VGH	Positive power for TFT
39	LED+	LED Anode
40	LED+	LED Anode

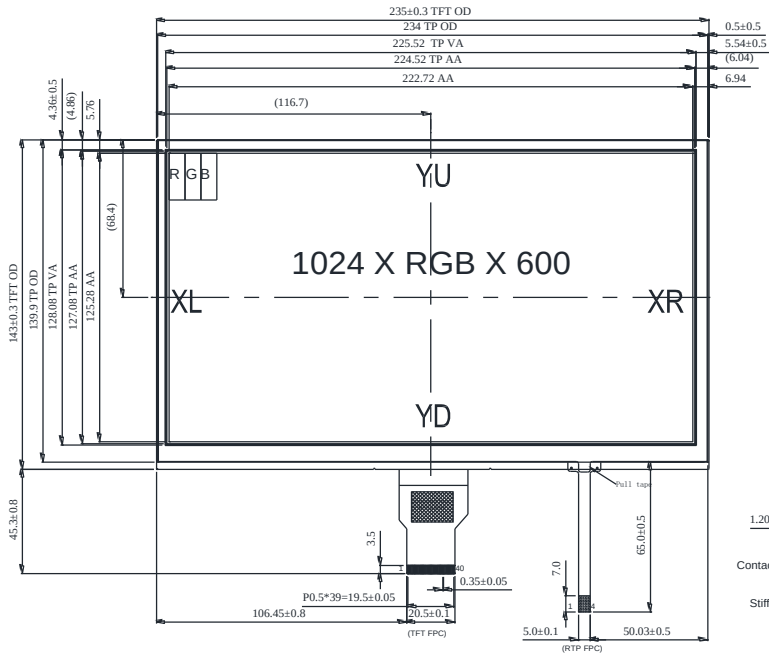
When L/R=”0”,set right to left scan direction.
When L/R=”1”,set left to right scan direction.
When U/D=”0”,set top to bottom scan direction.
When U/D=”1”,set bottom to top scan direction.

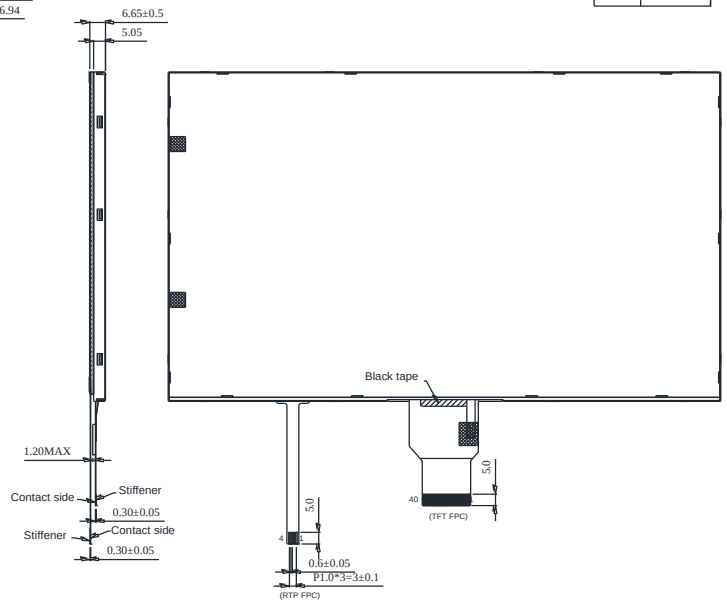
Touch Panel Information



PIN	Logic
1	YU
2	XL
3	YD
4	XR

Contour Drawing





RTP FPC

PIN NO.	SYMBOL
1	YU
2	XL
3	YD
4	XR

TFT FPC

PIN NO.	SYMBOL	PIN NO.	SYMBOL
1	VCOM	21	RXin3+
2	VDD	22	GND
3	VDD	23	NC
4	NC	24	NC
5	Reset	25	GND
6	STBYB	26	NC
7	GND	27	NC
8	RXin0-	28	SELB
9	RXin0+	29	AVDD
10	GND	30	GND
11	RXin1-	31	LED-
12	RXin1+	32	LED-
13	GND	33	L/R
14	RXin2-	34	U/D
15	RXin2+	35	VGL
16	GND	36	NC
17	RXCLKN-	37	NC
18	RXCLKN+	38	VGH
19	GND	39	LED+
20	RXin3-	40	LED+

The non-specified tolerance of dimension is ±0.3 mm .