Winstar Display Co., LTD
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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WH2004D-TMI-ETV#

APPROVED BY:	
(FOR CUSTOMER USE ONLY)	**PCB VERSION:** **DATA:**

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2010.04.08		First issue

Represented by:

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2.Precautions in use of LCD Modules

(1)Avoid applying excessive shocks to the module or making any alterations or modifications to it.
(2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
(3)Don't disassemble the LCM.
(4)Don't operate it above the absolute maximum rating.
(5)Don't drop, bend or twist LCM.
(6)Soldering: only to the I/O terminals.
(7)Storage: please storage in anti-static electricity container and clean environment.
(8). Winstar have the right to change the passive components
(9). Winstar have the right to change the PCB Rev.

3.General Specification

Item	Dimension	Unit
Number of Characters	20 characters x 4Lines	—
Module dimension	77.0 x 47.0 x 11.9(MAX)	mm
View area	60.0 x 22.0	mm
Active area	55.1 x 17.56	mm
Dot size	0.46 x 0.42	mm
Dot pitch	0.51 x 0.47	mm
Character size	4.03 x 2.3	mm
Character pitch	4.51 x 2.78	mm
LCD type	STN Negative, Blue Transmissive, (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)	
Duty	1/16	
View direction	6 o'clock	
Backlight Type	LED White	

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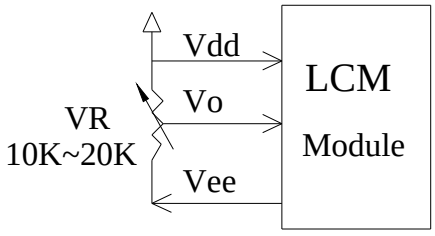
4.Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	℃
Storage Temperature	T_{ST}	-30	—	+80	℃
Input Voltage	V_I	V_{SS}	—	V_{DD}	V
Supply Voltage For Logic	V_{DD}-V_{SS}	-0.3	—	7	V
Supply Voltage For LCD	V_{DD}-V_0	-0.3	—	13	V

5.Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	V_{DD}-V_{SS}	—	2.7	3.0	3.3	V
Supply Voltage For LCD *Note	V_{DD}-V_0	Ta=-20℃	—	—	5.3	V
		Ta=25℃	—	4.5	—	V
		Ta=70℃	3.8	—	—	V
Input High Volt.	V_{IH}	—	0.7 V_{DD}	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	V_{SS}	—	0.6	V
Output High Volt.	V_{OH}	—	0.7 V_{DD}	—	—	V
Output Low Volt.	V_{OL}	—	—	—	0.2 V_{DD}	V
Supply Current	I_{DD}	V_{DD}=3.0V	1.0	1.2	1.5	mA

* Note: Please design the VOP adjustment circuit on customer's main board

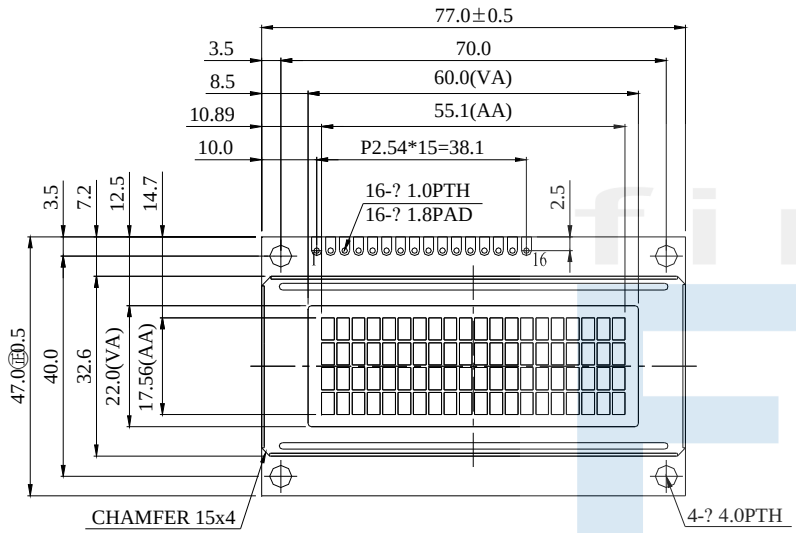


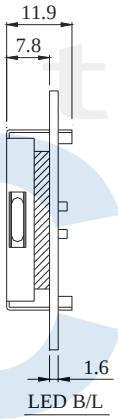
7.Interface Pin Function

Pin No.	Symbol	Level	Description
1	V_{SS}	0V	Ground
2	V_{DD}	3.0V	Supply Voltage for logic
3	VO	(Variable)	Operating voltage for LCD
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(MPU→Module) L: Write(MPU→Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bus line
8	DB1	H/L	Data bus line
9	DB2	H/L	Data bus line
10	DB3	H/L	Data bus line
11	DB4	H/L	Data bus line
12	DB5	H/L	Data bus line
13	DB6	H/L	Data bus line
14	DB7	H/L	Data bus line
15	Vee	—	Negative Voltage Output
16	NC	—	NC

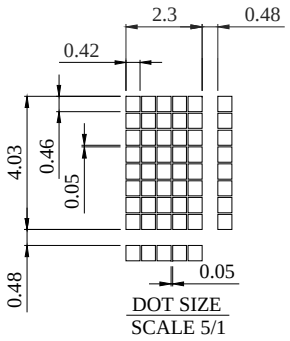
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8.Contour Drawing &Block Diagram





PIN NO.	SYMBOL
1	Vss
2	Vdd
3	Vo
4	RS
5	R/W
6	E
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	Vee
16	NC



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